

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:		)
		)
	Nobuhiro Nunoya et al.	)
		)
Serial No.:	10/527,355	) Art Unit
		) 2828
Filing Date:	March 7, 2005	)
		)
Confirmation No.:	4938	)
		)
For:	OPTICAL SEMICONDUCTOR DEVICE AND	)
	OPTICAL SEMICONDUCTOR INTEGRATED	)
	CIRCUIT	)
		)
Examiner:	Patrick Stafford	)

RESPONSE TO FINAL OFFICE ACTION

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Examiner:

Applicant submits this paper in response to the Office Action mailed March 19, 2009.  
Reconsideration is respectfully requested in view of the following remarks:

**Remarks** begin on page 2 of this paper.